Second Call for Papers **(Extended)**

2020 International Symposium on
VLSI Design, Automation and Test (2020 VLSI-DAT)

The 2020 International Symposium on VLSI Design, Automation and Test will be held on April 20-23, 2020 at the Ambassador Hotel, Hsinchu, Taiwan. Original and unpublished papers on all aspects of VLSI Design, Automation and Test are solicited, including but not limited to:

ANALOG DESIGN	DIGITAL DESIGN	EDA	TEST
RF, Analog and Mixed Signal Circuits	Digital Circuits and ASICs	Logic and Behavioral Synthesis	Test Generation and Compression
Sensor and Interface Circuits	CPU, DSP and Multicore Architectures	Physical Design and Verification	Design-for-Testability and BIST
Memory Circuits and Systems	Multimedia Processing Designs	Design for Manufacturability	RF, Analog and Mixed-Signal Test
Biomedical Circuits	Communication Designs	Power/Thermal Estimation and Optimization	Memory Test
Energy-Harvesting and Power Management Circuits	Hardware Security and Trust	Design Verification	SOC and System Level Test
Ultra Low-Power Circuits and Systems	Designs for Edge Computing	Modeling and Simulation	Silicon Debug and Diagnosis
Memristive and Neuromorphic Circuits	Designs for Machine Learning	Electronic System Level Design	3D IC and Interposer-Based IC Test
Security Circuits for IoT and AI	SOC and NOC Architectures	Hardware/Software Co-Design	Yield and Reliability Enhancement
	Embedded System and Software	Machine Learning for EDA	On-Chip Monitoring
	System-in-Package Designs	Analog EDA	Test Data Mining and Learning
		EDA for Microfluidic Biochips	Test Standards

➲ GENERAL INSTRUCTIONS

• Prospective authors must submit a self-contained paper with figures and tables electronically through the conference website **Submit your paper!** by **October 30**, 2019 (23:59 GMT+8).

• **The length of a submitted paper must be 2-4 pages.** Any submissions not adhering to the length constraint will be returned immediately without review.

• In addition to the paper and camera-ready manuscript **CLICK HERE**, the submission should also include an 80~100-word ABSTRACT, which will be published in the advance and final program if the paper is accepted.

• VLSI-DAT adopts the DOUBLE BLIND REVIEW process; please **DO NOT reveal** your name(s) or affiliation(s) anywhere in the submitted manuscript for the first paper submission.

Second Call for Papers (Extended)

• Before submitting your abstract/paper, please review the information on IEEE Intellectual Property Rights at http://www.ieee.org/web/publications/rights/index.html

• The notices of acceptance will be sent out to authors on December 31, 2019.

• Any changes on title and author list or withdraw after acceptance must be approved by Technical Program Co-Chairs.

• For an accepted paper to be published in the proceedings, one of its co-author(s) MUST **pay the full registration fee** by February 29, 2020 23:59 (GMT+8) and attend the symposium to present the paper. Note that each accepted paper shall be accompanied by a different full registration; that is, two full registrations are required for presenting two papers even if the presenter is the same.

• Presentation of accepted papers at the symposium must be in English and will be limited to 18 minutes with an additional 2 minutes for Q&A. The final manuscripts of all accepted papers will be published as submitted in the proceedings.

• No-show papers will not be included in the symposium proceedings and will not be submitted to the IEEE Xplore database.

➲ STUDENT SUBSIDY **MORE INFO**

• All students are entitled to get a discount of up to 85% on the registration fee.

• Financial supports are available for full-time student presenters living outside of Taiwan.

➲ BEST PAPER AWARD **MORE INFO**

Two best papers will be selected this year through a rigorous evaluation process, which is conducted by the program committee and session chairs.

➲ IMPORTANT DATES

Note: all are based on Taiwan time (GMT+8).

Paper Submission Deadline(Extended)	~~Oct. 15, 2019~~ October 30, 2019
Notification of Acceptance	December 31, 2019
Final Paper (IEEE compatible version) Submission Deadline	January 31, 2020
Author Registration Deadline	February 29, 2020

2020 Technical Program Co-Chairs

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